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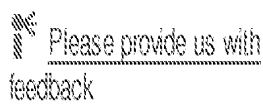
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4 A global communication optimization technique based on data-flow analysis and linear algebra

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 November 1999 **Transactions on Programming Languages and Systems (TOPLAS)** , Volume 21 Issue 6
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Andrew K. Wright, Robert Cartwright

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7 [Fast incremental maintenance of approximate histograms](#)

 [Phillip B. Gibbons](#), [Yossi Matias](#), [Viswanath Poosala](#)
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8 [Model checking multi-agent systems with MABLE](#)

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Keywords: logic programming, multi-agent reasoning, probabilistic reasoning, uncertainty

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We describe an initiative under way at Warwick to provide a technical foundation for computer-aided learning and computer-assisted assessment tools, which allows a rich dialogue sensitive to individual students' response patterns. The system distinguishes ...

Keywords: CAA, CAL, DTD, XML, markup

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 Youngsoo Shin, Daehong Kim, Kivoung Choi

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Keywords: abstract interpretation, alias analysis, dataflow analysis, destructive updating, pointer analysis, shape analysis, shape graphs, static analysis

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 Daniel A. Jiménez, Calvin Lin

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This article presents a new and highly accurate method for branch prediction. The key idea is to use one of the simplest possible neural methods, the perceptron, as an alternative to the commonly used two-bit counters. The source of our predictor's accuracy ...

Keywords: Branch prediction, neural networks

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 Roy T. Fielding, Richard N. Taylor

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The World Wide Web has succeeded in large part because its software architecture has been designed to meet the needs of an Internet-scale distributed hypermedia application. The modern Web architecture emphasizes scalability of component interactions, ...

Keywords: Network-based applications, REST, World Wide Web

17 On type systems for object-oriented database programming languages◆ Yuri Leontiev, M. Tamer Özsu, Duane Szafron

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The concept of an object-oriented database programming language (OODBPL) is appealing because it has the potential of combining the advantages of object orientation and database programming to yield a powerful and universal programming language design. ...

Keywords: OODB, OODBPL, object-oriented database programming language, type checking, typing

18 Design issues for general-purpose adaptive hypermedia systems◆ Hongling Wu, Erik de Kort, Paul De BraSeptember 2001 **HYPERTEXT '01: Proceedings of the 12th ACM conference on Hypertext and Hypermedia****Publisher:** ACM ◆ [Request Permissions](#)Full text available:  Pdf (290.99 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 36, Downloads (Overall): 582, Citation Count: 11

A hypermedia application offers its users much freedom to navigate through a large hyperspace. For authors finding a good compromise between offering navigational freedom and offering guidance is difficult, especially in applications that target a broad ...

Keywords: active databases, adaptation rules, adaptive hypermedia, confluence, termination, user modeling

19 Interprocedural pointer alias analysis◆ Michael Hind, Michael Burke, Paul Carini, Jong-Deok ChoiJuly 1999 **Transactions on Programming Languages and Systems (TOPLAS)** , Volume 21 Issue 4**Publisher:** ACM ◆ [Request Permissions](#)Full text available:  Pdf (502.42 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 142, Downloads (Overall): 1057, Citation Count: 37

We present practical approximation methods for computing and representing interprocedural aliases for

a program written in a language that includes pointers, reference parameters, and recursion. We present the following contributions: (1) a framework ...

Keywords: interprocedural analysis, pointer aliasing, program analysis

20 A text-compression-based method for code size minimization in embedded systems

 Stan Liao, Srinivas Devadas, Kurt Keutzer

January 1999 **Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 4 Issue 1

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We address the problem of code-size minimization in VLSI systems with embedded DSP processors. Reducing code size reduces the production cost of embedded systemswe use data-compression methods to develop code-size minimization strategies. In our framework, ...

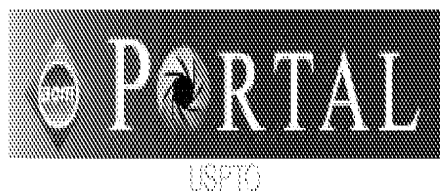
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Keywords: communication optimizations, data-flow analysis, distributed-memory machines, global optimizations, message vectorization, parallelism

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We present practical approximation methods for computing and representing interprocedural aliases for a program written in a language that includes pointers, reference parameters, and recursion. We present the following contributions: (1) a framework ...

Keywords: interprocedural analysis, pointer aliasing, program analysis

16 A text-compression-based method for code size minimization in embedded systems Stan Liao, Srinivas Devadas, Kurt KeutzerJanuary 1999 **Transactions on Design Automation of Electronic Systems (TODAES)** , Volume 4 Issue 1**Publisher:** ACM  [Request Permissions](#)Full text available:  [Pdf](#) (184.16 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 33, Downloads (Overall): 436, Citation Count: 22

We address the problem of code-size minimization in VLSI systems with embedded DSP processors. Reducing code size reduces the production cost of embedded systemswe use data-compression methods to develop code-size minimization strategies. In our framework, ...

Keywords: code size optimization, compression**17** A database model for object dynamicsM. P. Papazoglou, B. J. KrämerMay 1997 **The VLDB Journal — The International Journal on Very Large Data Bases** , Volume 6 Issue 2**Publisher:** Springer-Verlag New York, Inc.Full text available:  [Pdf](#) (313.64 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)**Bibliometrics:** Downloads (6 Weeks): 0, Downloads (12 Months): 39, Downloads (Overall): 376, Citation Count: 8


To effectively model complex applications in which constantly changing situations can be represented, a database system must be able to support the runtime specification of structural and behavioral nuances for objects on an individual or group basis. ...

Keywords: Dynamic class hierarchy, Dynamic object re-classification, Object migration, Object role model, Object-oriented database systems**18** Search and strategies in OPL Pascal Van Hentenryck, Laurent Perron, Jean-Francois PugetOctober 2000 **Transactions on Computational Logic (TOCL)** , Volume 1 Issue 2**Publisher:** ACM  [Request Permissions](#)Full text available:  [Pdf](#) (169.78 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 90, Downloads (Overall): 688, Citation Count: 9

OPL is a modeling language for mathematical programming and combinatorial optimization. It is the first language to combine high-level algebraic and set notations from mathematical modeling languages with a rich constraint language and the ability to ...

Keywords: combinatorial optimization, constraint programming, modeling languages, search

19 Clarifying the fundamentals of HTTP

 Jeffery C. Mogul

May 2002 **WWW '02: Proceedings of the 11th international conference on World Wide Web**

Publisher: ACM


Full text available:  PDF (157.39 KB) Additional Information: full citation, abstract, references, cited by, index terms

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The simplicity of HTTP was a major factor in the success of the Web. However, as both the protocol and its uses have evolved, HTTP has grown complex. This complexity results in numerous problems, including confused implementors, interoperability failures, ...

Keywords: HTTP, protocol design

20 Register tiling in nonrectangular iteration spaces

 Marta Jiménez, José M. Llaberia, Agustín Fernández

July 2002 **Transactions on Programming Languages and Systems (TOPLAS)**, Volume 24 Issue 4

Publisher: ACM  Request Permissions


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Loop tiling is a well-known loop transformation generally used to expose coarse-grain parallelism and to exploit data reuse at the cache level. Tiling can also be used to exploit data reuse at the register level and to improve a program's ILP. However, ...

Keywords: Data reuse, locality, loop optimization, loop tiling, register level

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